

ABSTRACT

A signal separation circuit (11) normally outputs a main signal, but outputs a TMCC signal when an error detection and
5 correction unit (4) sets a completion flag. Main signals are input
via a selection circuit (3) to an execution unit (4), where error
correction of the main signals is performed. The error-corrected
main signals are supplied via a switching circuit (8) to a first
data output circuit (9). Meanwhile, TMCC signals are input via
10 the selection circuit (3) to an execution unit (4), error correction
is carried out to the TMCC signals. The error-corrected TMCC
signals are supplied via switching circuit (8) to a second data
output circuit (10).